



**PATENT**  
**5119-07901**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application No.: 60/291,664  
Filed: May 16, 2001  
Inventor(s):  
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Sreenivasan and Roger T.  
Bonnecaze

Title: METHOD AND SYSTEM  
FOR FABRICATING  
NANOSCALE PATTERNS  
IN LIGHT CURABLE  
COMPOSITIONS USING  
AN ELECTRIC FIELD

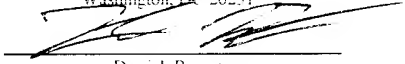
§ Examiner: Unknown  
§ Group/Art Unit: Unknown  
§ Atty. Dkt. No: 5119-07901

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Derrick Brown

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
Washington, D.C. 20231

Sir:

Please amend the above-captioned application as follows:

***In the Specification:***

Listed below is a clean copy of substitute paragraphs that replace the referenced paragraphs of the specification. A marked-up copy of the amended paragraph is provided in an accompanying document.

At page 4, lines 3-12:

An electric field may be applied between the template and the substrate. The application